Simultaneous Enhancement of Electron Injection and Air Stability in N-Type Organic Field-Effect Transistors by Water-Soluble Polyfluorene Interlayers

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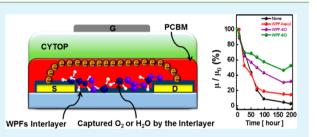
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Supporting Information

ABSTRACT: Here, we report the simultaneous attainment of efficient electron injection and enhanced stability under ambient conditions for top-gate/bottom-contact (TG/BC), n-type, organic field-effect transistors (OFETs) using water-soluble polyfluorene derivatives (WPFs). When inserting the WPF interlayers between a semiconductor and the BC Au electrodes, initially the ambipolar (6,6)-phenyl-C₆₁butyric acid methyl ester (PCBM) OFETs were fully converted to unipolar charge transport characteristics that were exclusively n-type with significantly increased electron mobilities as



high as $0.12 \text{ cm}^2/(\text{V s})$ and a decreased threshold voltage. These improvements were mostly attributed to the interfacial dipoles of WPF layers that aligned to form a favorable energy band structure for efficient electron injection and to effectively block counter charge carriers. These were confirmed when values for the reduced work function of metal electrodes with WPFs and their correlated contact resistance were measured via the ultraviolet photoemission spectroscopy and the transmission-line method, respectively. Moreover, the WPF interlayers played an important role in air stability of PCBM OFETs that exhibited higher and appreciably enhanced by increasing the ethylene-oxide side chain lengths of WPFs, which presumably was due to the water/oxygen/ion capturing effects in the hydrophilic interlayers.

KEYWORDS: organic field-effect transistors, conjugated polyelectrolytes, charge injection, interfacial dipoles, air-stability

■ INTRODUCTION

Over the past two decades, π -conjugated organic molecules have been extensively studied based on the complementary aspects of scientific interest and industrial benefit. These materials possess unique charge generation as well as transport and recombination properties that show promise for optoelectronics applications such as energy harvesting (organic photovoltaics (OPVs)),¹⁻³ switching (organic field-effect transistors (OFETs)),⁴⁻⁶ and light-emitting devices (organic light-emitting diodes (OLEDs) and lasers).⁷ Moreover, these qualities enable the development of inexpensive soft electronics applications that result mostly from low-temperature and solution processability using a variety of cost-effective, largearea, high throughput graphic art printing techniques.^{8,9} In developing high-performance organic electronic devices, a number of approaches have been applied including the synthesis of new materials,^{10,11} post treatments for improving thin-film quality,¹² and interface engineering.^{13,14} The development of optimized materials that include active, electrode, and dielectric layers is likely to be the best solution in order to realize high device performance.

However, it is quite difficult to simultaneously satisfy all requirements for commercial products such as OFETs with environmental/bias stability and organic semiconductors that have high charge carrier mobility, negligible interface traps, and small off-current, Ohmic contact to charge injection electrodes. Alternatively, interface engineering is one of the strategies that shows promise for the achievement of high-performance optoelectronic devices by extracting the maximum characteristics from a given material via surface treatment (e.g., oxygen plasma and self-assembled monolayers¹⁵) and the insertion of interlayers such as poly(3,4-ethylenedioxythiophene):poly(styrenesulfonate) (PEDOT:PSS),^{16,17} polyelectrolytes,^{18,19}

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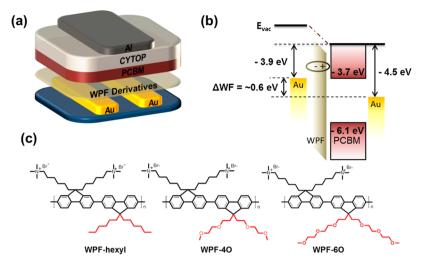


Figure 1. (a) Schematic diagram of a TG/BC OFET structure with WPF interlayers, (b) energy level diagram at the interface between semiconductor and Au S/D electrodes, and (c) the molecular structures of the WPF derivatives.

and inorganic compounds,^{20,21} between the contact electrodes and organic semiconductors.

Water-/alcohol-soluble conjugated polymers or conjugated polyelectrolytes (CPEs) have also been used in research as interlayers for various organic optoelectronic devices²² such as OLEDs,^{23,24} OPVs,^{25,26} and OFETs.²⁷ These interlayers have fulfilled critical roles in reducing the energy barriers for efficient charge injection/transport in many organic optoelectronic devices,²² which has been attributed mainly to an effectively tuned work function of metal electrodes that is induced either by permanent interfacial dipoles²⁸ or by ion migration²⁹ under the application of an electric field. Moreover, the high solubility of the CPEs in aqueous/alcoholic polar solvents easily allows the fabrication of multilayered devices via simple solution processing with no damage or intermixing with under-laid layers. Despite these many advantageous features of CPEs, a significant degradation in device performance has been noted after exposure to humid air because of the hydrophilic and hygroscopic nature of the CPEs. For this reason, there has been poorly understood and never been a report that documents how the structural feature and properties of a CPEs could affect the environmental stability of organic electronic devices over time under an ambient atmosphere.

As far as we could ascertain, this report is the first to document the simultaneous enhancement of device performance and air stability for n-type (6,6)-phenyl-C₆₁ butyric acid methyl ester (PCBM) OFETs through the introduction the water-soluble polyfluorene derivatives (WPFs) as interlayers between semiconductor and bottom-contact (BC) source/ drain electrodes. Initially, top-gated PCBM OFETs showed ambipolar charge transport characteristics with electron mobilities ($\mu_{\text{FET},e}$) of ~0.045 cm²/(V s) and weak hole transport properties of $\mu_{\rm FET,h} \approx 0.003 \ {\rm cm}^2/({\rm V~s})$. On the other hand, the WPFs played an important role as an efficient electron-injection/hole-blocking layer, so that the PCBM OFETs fully converted to n-type only unipolar devices with a 3-fold increase in electron mobility of ~0.12 cm²/(V s). Moreover, we also noted that the PCBM OFETs with WPF interlayers showed an increased stability under ambient air conditions compared with devices with no interlayer. Interestingly, the air stability improved as the ethylene-oxide side chains of the WPFs increased, which could be mainly

attributed to the water/oxygen/ion capturing effects by hygroscopic/hydrophilic WPF moieties that prevented the trapping of mobile electrons at the semiconductor-dielectric interface.

EXPERIMENTAL METHODS

Field-Effect Transistor Fabrication. Corning Eagle XG glass was used as a substrate after sequential cleaning in an ultrasonic bath with deionized water, then acetone, then isopropanol. Source and drain electrodes of Au/Ni were patterned on substrates via conventional photolithography processing. WPF derivatives, WPF-hexyl, WPF-4O, and WPF-6O, were synthesized in our laboratory.48 The WPF interlayers were dissolved in methanol and were deposited by spincoating onto the Au-patterned substrates in a N₂-filled glovebox. The organic semiconductor material, PCBM was purchased from Nano-C and used as-received. The PCBM was dissolved in anhydrous chlorobenzene to make a 10 mg/mL solution that was then spincoated. The semiconductor films were thermally annealed at 110 °C for 20 min to remove residual solvent under N2 inert conditions. For a polymer dielectric layer, a CYTOP solution (Asahi Glass) was spincoated onto the semiconductor layer. To remove the solvent, the films were baked for 2 h at 80 °C in the same N2-filled glovebox. The Al gate electrode was deposited by thermal evaporation to achieve a film that was 50 nm thick with a shadow mask to complete fabrication of the TG/BC transistor.

Thin Film and Device Characterization. The thickness of the WPF layers and the surface morphology of the PCBM were investigated via tapping-mode AFM (Nanoscope III, Veeco Instruments, Inc.). The UPS measurement was carried out using an AXIS-NOVA (Kratos, Inc.) with a base pressure of 4.2×10^{-9} Torr. The current–voltage (*IV*) characteristics of the OFET devices were measured with a KEITHLEY 4200-SCS under nitrogen conditions at room temperature. The capacitance of the CYTOP was measured using an Agilent 4284A precision LCR meter and a KEITHLEY 4200-SCS at a frequency of 10 kHz. Air-stability was monitored by measuring the *IV* characteristics after keeping the OFET devices under an air atmosphere with humidity of 25%.

RESULTS AND DISCUSSION

Figure 1a shows a schematic diagram of the TG/BC OFET device structure. The staggered structure is a good configuration to use in discerning the charge injection effects through the interlayers on single metal electrodes because of the relatively low contact resistance (R_c) as a result of the large physical contact area and reduced current crowding effects

compared with the other coplanar device structures.³⁰ Moreover, hydroxyl-free polymer gate dielectrics³¹ in top-gated devices enable the study of nearly intrinsic ambipolar charge transport characteristics via excellent organic material compatibility and self-passivation effects³² compared with exposed active-layer, bottom-gate devices. To fabricate TG/BC OFETs based on solution-processed PCBM, we chose a CYTOP gate dielectric because the CYTOP will dissolve only in fluorinated orthogonal solvents that are perfect for the under-layer of most organic semiconductor layers including PCBM. Moreover, CYTOP has a low *k*, chemical stability, low surface energy, and hydrophobic dielectric properties, which allows it to provide excellent interfacial states that exhibit intrinsic charge transport properties even in commonly known unstable n-type semiconductors, such as PCBM.³³

As shown in Figure 2a, the transfer characteristics (the drain current (I_d) vs the gate voltage (V_g)) of the PCBM OFETs

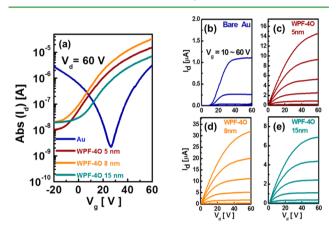


Figure 2. (a) Transfer characteristics of the n-channel in PCBM OFETs as the thickness of the WPF-4O interlayer increases. Output characteristics of the n-channel in PCBM OFETs (b) without a WPF-4O interlayer, and with interlayers of (c) 5, (d) 8, and (e) 15 nm ($W/L = 1.0 \text{ mm}/20 \mu\text{m}$).

using pristine Au electrodes showed clear ambipolar features with electron ($\mu_{FET,e}$) and hole mobilities ($\mu_{FET,h}$) of ~0.045 and ~0.003 cm²/(V s), respectively, which were confirmed by typical V-shaped transfer curves in ambipolar transistors.³⁴ It was verified that our top-gated PCBM OFET devices can induce both electron (for n-type operation) and hole (for ptype operation) accumulations at the PCBM-CYTOP interface.³⁵ Notably, both electron and hole carriers could be injected from bare Au electrodes (measured metal work function $\Phi = -4.5$ eV) into the lowest unoccupied molecular orbital (LUMO = -3.7 eV) and the highest occupied molecular orbital (HOMO = -6.1 eV) levels, respectively, of a PCBM, and with appreciable electron and hole injection barrier heights of ~0.8 eV and ~1.6 eV, respectively (see Figure 1b). The output curve (the $I_d vs$ the drain voltage (V_d)) in Figure 2b also shows nonlinear behavior in the low V_d region, which is a clear evidence of the existence of a large R_c .

Figure 1c shows the molecular structures of the WPF interlavers with different ethylene oxide side chains: WPFhexyl, -4O, and -6O. After inserting the WPFs between the BC Au source/drain (S/D) electrodes and the PCBM semiconductor layer, the initial ambipolar characteristics of the PCBM OFET devices were fully converted to an n-type only unipolar operation regime, as shown in Figure 2a. The $\mu_{\text{FET},e}$ of the PCBM OFETs with a thickness-optimized (~8 nm) WPF-40 interlayer exhibited an approximate 3-fold increase (as high as 0.12 cm²/(V s)) and a reduced threshold voltage ($V_{th,e}$) of ~ 10.5 V. These developments were mostly attributed to the electron-injection and hole-blocking properties of the inserted WPF interlayers. Furthermore, it would also be attributed to the doping by ionic species which could act as a dopant.¹⁸ To determine the optimal thickness for an interlayer, we increased the WPF thickness by as much as 15 nm and found that the I_d and $\mu_{\text{FET},e}$ in n-channel saturation region (at $V_{\text{d}} = 60$ V) were gradually enhanced at an interlayer thickness of ~8 nm. Beyond this critical thickness, however, the PCBM OFETs exhibited a degraded performance both in p-channel and n-channel operation regimes. The measured $\mu_{\text{FET,e}}$ of the PCBM OFETs with a 15 nm thick WPF-4O interlayer was decreased to 0.023 $\text{cm}^2/(\text{V s})$, which was an even lower value than that of a bare Au device with no interlayer. Fundamental device parameters such as $\mu_{\text{FET,e}}$, V_{th} , and the on/off-current ratios $(I_{\text{on}}/I_{\text{off}})$ of the PCBM OFETs with WPF-4O interlayer thicknesses that differed from 5 to 15 nm are summarized in Table 1.

Although there was a slight increase in the root-mean-square roughness (R_{n}) of from 0.24 to 0.40 nm with an increase in the interlayer film thickness of from 5 to 15 nm, respectively, we found no distinctive morphological changes in the atomic force microscopy (AFM) images of the surfaces of spin-coated PCBM active films either those with or without WFP-4O layers (see Figure 3). The π -conjugated organic molecules reportedly had a better-ordered morphology either on carbon-based electrodes³⁶ or on self-assembled monolayer-treated metal $\mathsf{electrodes}^{37}$ than on the bare surface, which typically resulted in a reduced R_c and improved charge transport characteristics. Because of the similarities in the morphological features shown in the AFM images of the same semiconductor films-those with a bare Au and those with an interlayer-the dramatic enhancements of the performance of the n-type OFETs with a WPF interlayer could have been mostly the result of the modification of the interfacial energy states between the semiconductor and the BC electrodes.

Panels a and b in Figure 4 show the binding energy of electrons on an Au electrode surface from ultraviolet photoemission spectroscopy (UPS) and the measured value

Table 1. Device Parameters of the PCBM OFETs as the Thickness of WPF-4O is $Increased^a$

active layer	interlayer (thickness)	$\mu_{\rm FET,e}~({ m cm}^2/({ m V~s}))$	$I_{\rm on}/I_{\rm off}$	$V_{\rm th,e}~({ m V})$	$R_{\rm c}W$ (M Ω cm)	Φ (eV)
PCBM	none	$0.046 (\pm 0.01)$	$\sim 1 \times 10^4$	29.3 (± 4.28)	18.65	4.5
	WPF-40 (5 nm)	$0.084 (\pm 0.016)$	$\sim 1 \times 10^{3}$	16.8 (± 4.85)	0.12	3.9
	WPF-40 (8 nm)	$0.12 (\pm 0.03)$	$\sim 1 \times 10^{3}$	10.5 (± 2.68)	0.02	3.9
	WPF-4O (15 nm)	$0.023 (\pm 0.009)$	$\sim 1 \times 10^2$	6.9 (± 2.71)	0.09	3.8

"The electron field-effect mobilities ($\mu_{\text{FET,e}}$) were calculated in the saturation region ($V_{\text{d}} = 60 \text{ V}$) using a standard device formula. CYTOP gate dielectric was ~500 nm thick ($C_{\text{i}} \approx 3.72 \text{ nF cm}^{-2}$).

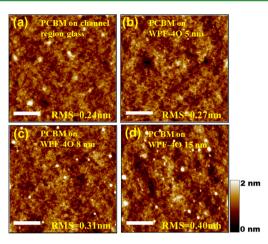


Figure 3. Height mode AFM images $(2 \ \mu m \times 2 \ \mu m)$ of PCBM films (a) on a channel region without WPFs, (b) on a 5 nm thick WPF-4O layer, (c) on an 8 nm thick WPF-4O layer, (d) on a 15 nm thick WPF-4O layer. The scale bars are 500 nm.

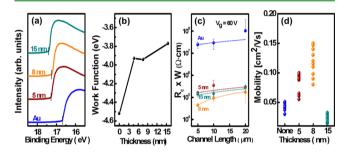


Figure 4. (a) UPS spectra, and (b) corresponding work functions of bare Au electrodes and those of WPF-4O layers with a different thickness on Au. (c) Channel width normalized contact resistance (R_cW) for electron injection, and (d) the distribution map of electron mobility ($\mu_{\text{FET},e}$) in PCBM OFETs based on bare Au electrodes and those of WPF-4O layers with a different thickness on Au.

of the work function (Φ) of metal electrodes as the thickness of WPF interlayers changes. The Φ of an Au electrode was remarkably reduced from -4.5 to -3.8 eV, as the WPF-4O layer thickness was increased from 0 to 15 nm. Notably, the thickness-optimized (~8 nm thick) WPF-4O laver induced the Φ of an Au electrode to a level of -3.9 eV, which matched well with the value of the LUMO level of a PCBM, at about -3.7eV.³⁸ The R_c of PCBM OFETs for electron injection were evaluated using the transmission-line method (TLM) at V_{σ} = 60 V. As shown in Figure 4c, the R_c of PCBM OFETs with a bare Au electrode was ~18.65 M Ω cm, but it was significantly reduced by approximately 3 orders of magnitude to 20 k Ω cm after the insertion of a WPF interlayer. The decreased R_c of WPFs-incorporated PCBM OFETs was attributed to a diminished electron injection barrier height between the LUMO level of PCBM and the Φ of Au electrodes (see schematic energy level diagram in Figure 1b). The improved electron injection efficiency was also confirmed in n-channel output characteristics, which showed output curves that were almost linear in the low V_d region, as shown in Figure 2c-e. In addition, the negative shift of V_{th} can be also explained by reducing $R_c^{39,40}$ because of the WPF-4O interlayer, which can substantially affect to the charge density or accumulation in the semiconductor. This indicates a clear reduction in the R_c by incorporating the WPFs as an electron injection layer. Therefore, the WPF interlayers make the top-gated PCBM

OFETs an n-type only unipolar device with high electron mobility and negligible p-type behaviors (see Figure 4d).

Note that π -conjugated polyelectrolyte (CPE) layers have typically played an important role in modifying the Φ of metal electrodes via ion migration and/or aligned permanent interfacial dipoles on the surface, thereby engineering the charge carrier injection/extraction properties in various optoelectronic devices.^{22,28,29} The interfacial dipoles in WPF interlayers are mostly induced between the two amine ionic salts or the ethylene oxide side chains,²⁵ where the dipole moment lies at the 9-position of WPF-4O and WPF-6O molecules.^{23,41} Most devices with a thin CPE layer (below 10 nm thick) could be affected by the interfacial dipoles,⁴² which agreed with our OFET results. Thin WPF-4O layers (5-8 nm thick) formed interfacial dipoles at the interface between the BC Au electrodes and the PCBM n-type semiconductor, which shifted up the vacuum level of Au electrodes and provided a favorable dipolar direction that enhanced the electron injection (see Figure 1b). Although very similar linear output characteristics persisted in the low V_d regions device performance of the OFETs with a thicker WPF-4O interlayer (about 15 nm thick) exhibited a relatively low $\mu_{\text{FET},e}$ of ~0.02 cm²/(V s), which was even below that of bare Au OFET devices. This could be explained by the interrupted charge carrier injection into a semiconductor active channel with a thickness that was beyond the critical interlayer amount (above ~8 nm), at which point the bulk insulating properties of the material started to dominate.^{15,43} In cases of overly thick WPF-4O interlayers (40-50 nm thick), the transistors were not operational.

Various WPFs with different ethylene oxide side chains from WPF-hexyl, -4O, and -6O (as shown in Figure 1c) were adopted as interlayers of the same OFETs in order to investigate how the different length would affect device performance. In our previous study, longer ethylene oxide side chains generally improved the power conversion efficiency in OPV devices due to an increase open circuit voltages by a reduction in the Φ of metallic cathodes.²⁵ The optimal thickness for a WPF-4O interlayer (8 nm thick) was also applied to the other interlayers: WPF-hexyl and WPF-6O. The PCBM OFETs showed similar device performances regardless of the side chains, as noted by the transfer characteristics shown in Figure 5a. The $\mu_{\text{FET,e}}$ of PCBM OFETs with all types of WPF interlayers were improved to ~0.10 $\text{cm}^2/(\text{V s})$, whereas the $V_{\rm the}$ of the corresponding devices was gradually decreased from 10.8 to 5.8 V as ethylene oxide side chain length was increased (see Table 2). All WPF interlayers tuned the Φ of an Au

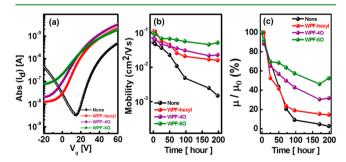


Figure 5. (a) Transfer characteristics (at $V_d = 60$ V) of the n-channel in PCBM OFETs without and with WPF injection layers. Evolution of (b) the electron field-effect mobility and (c) its normalized value as a function of time for PCBM OFETs exposed to ambient air with ~25% humidity.

active layer	interlayer (thickness)	$\mu_{\rm FET,e}~({ m cm}^2/({ m V~s}))$	$I_{ m on}/I_{ m off}$	$V_{\rm th,e}~({ m V})$	Φ (eV)
PCBM	None	$0.046 (\pm 0.01)$	$\sim 1 \times 10^{4}$	$29.3 (\pm 4.28)$	4.3
	WPF-hexyl (8 nm)	$0.098 (\pm 0.025)$	$\sim 1 \times 10^{3}$	$10.8 (\pm 0.96)$	3.6
	WPF-4O (8 nm)	$0.12 (\pm 0.03)$	$\sim 1 \times 10^{3}$	$10.5 (\pm 2.68)$	3.6
	WPF-6O (8 nm)	$0.11 (\pm 0.029)$	1×10^2 to 1×10^3	5.8 (± 1.47)	3.6

^aThe electron field-effect mobilities ($\mu_{\text{FET},e}$) were calculated in the saturation region (at $V_d = 60$ V) using a standard device formula. The CYTOP gate dielectric was ~500 nm in thickness (dielectric capacitance $C_i \approx 3.72$ nF/cm²).

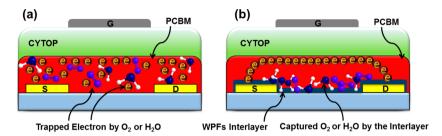


Figure 6. Schematic illustration of the air stability of TG/BC PCBM OFETs, (a) without and (b) with a WPF interlayer.

electrode to the same value that was confirmed by UPS measurement (see Figure S1 in the Supporting Information), so the negligible electron injection barrier heights for BC Au electrodes via all WPF interlayers with different side chains had already contributed to the similar amounts of enhancements in n-type OFET characteristics.

Stability under ambient air conditions for n-type OFETs is also a very important issue for practical optoelectronics applications. Our PCBM OFETs with a WPF interlayer were exposed to ambient air with ~25% humidity, and device characteristics were monitored for about 200 h. In comparison with p-type organic semiconductors, the OFET properties of the n-type materials were more easily affected by O2 and H2O and degraded fast by trapped mobile electrons in an active channel.^{44,45} Figures 5b and 5c show the evolution of $\mu_{\text{FET,e}}$ degradation in PCBM OFETs after exposure to air. For devices without the WPF interlayer, the $\mu_{\text{FET,e}}$ was rapidly degraded and finally reached $\sim 1 \times 10^{-3} \text{ cm}^2/(\text{V s})$ after 200 h exposure to air. It is noteworthy, that inserting the WPF interlayers significantly improved the air stability. In particular, the $\mu_{\rm FET,e}$ of a PCBM OFET using a WPF-6O layer was relatively stable and maintained about 0.05 $\text{cm}^2/(\text{V s})$ even after 200 h in air. Notably, by changing the ethylene oxide side chains from WPFhexyl to -4O and -6O, the overall performance of the n-type OFETs exhibited less degradation. The $\mu_{\text{FET},e}$ was stably maintained at higher values (exceeding $1 \times 10^{-2} \text{ cm}^2/(\text{V s})$) after exposure to air for 200 h. For a quantitative comparison of the device stability, the normalized by initial value of $\mu_{\text{FET,e}}$ are shown in Figure 5c. Without a WPF interlayer, the $\mu_{\text{FET,e}}$ of the PCBM OFETs was rapidly decreased to about 2% of the initial value after 200 h, whereas it remained at ~50% for the OFETs with a WPF-6O interlayer. The other devices with WPF-hexyl and WPF-4O interlayers also showed better air stabilities as ~15 and ~35%, respectively, of their initial values after 200 h. To the best of our knowledge, this is the first report of an improvement in the air stability of n-type OFETs by using a CPE electron-injection interlayer in the TG/BC device structure.

Although further study is needed to completely clarify the origin, there could be two possible origins of increasing air stability. First of all, we believe that the ammonium salt contained in the WPF interlayers could play as a role of dopant.

It can be identified by a clear evidence of n-doping that shows the increased off-current from the transfer curves.¹⁸ It has been reported that doping in n-channel OFETs by n-type dopants enhanced the device air-stability $^{46-48}$ and tetrabutyl ammonium salts induced efficient electron transfer to PCBM.⁴⁹ Therefore, it could be applied in our system that PCBM could be doped slightly by the ammonium salts in the WPF interlayers possibly increasing air-stability. However, it cannot be explained only by the doping of PCBM, which exhibited the better device air stability as increasing ethylene oxide side chain of the interlayers. Accordingly, we also believe that the hydrophilic and hygroscopic properties of the WPF molecules could effectively attract the polar oxygen and moisture molecules. Moreover, these interlayers may play the important role of a scavenger for the electron-trapping molecules $(O_2 \text{ and } H_2O)$ by preventing the diffusion of those molecules into the active channel region at the semiconductor-dielectric interface over time in air, as in Figure 6. Those capabilities improve when the hygroscopic interlayers have stronger oxygen- and moistureattracting groups in the CPE molecules. In our previous report,⁵⁰ we also investigated the side-chain effect on the performance of various optoelectronic devices, which proved that hydrophilic properties increase as the ethylene-oxide side chains increase.

In conclusion, we studied the effect of various WPFs used as charge-injection interlayers between n-type organic semiconductor and Au BC electrodes. Obviously, it was successfully verified that the PCBM OFET characteristics were remarkably improved by incorporating the WPF layers, which significantly enhanced the n-channel characteristics with a $\mu_{\text{FET,e}}$ that was increased to as high as 0.12 cm²/(V s), a reduced $V_{\text{th,e}}$ and a smaller R_c for electron injection, whereas the p-channel properties were suppressed. Very thin (~8 nm thick) WPF interlayers introduced interfacial dipoles on the metal electrode surface so that a reduced Φ enabled a negligible electron injection energy barrier height at the metal-semiconductor interface. It is worth noting that the WPF interlayers resulted in excellent air stability even in relatively air-unstable n-type semiconductors, such as PCBM, and also that increasing the ethylene oxide side chains of the interlayers exhibited better

device stability due to both doping via ammonium salt and a stronger capability for capturing the oxygen and moisture molecules. On the basis of these results, our WPF interlayers could be very attractive from the aspects of an efficient electron-injection/hole-blocking layer and a bottom-laid passivation layer for a variety of optoelectronics applications.

ASSOCIATED CONTENT

Supporting Information

UPS spectra of the various WPF derivatives interlayers with about 8 nm thickness on Au electrode surface, transfer characteristics as time goes by for 200 h without/with the interlayers, and contact angle of the WPF interlayers. This material is available free of charge via the Internet at http:// pubs.acs.org/

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Notes

The authors declare no competing financial interest.

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